

**PATENT NUMBER**

**PATENT DATE**

SCANNED 74B4 Q.A.

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/975946		3A 326	6380	2819	Chang

APPLICANTS  
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TITLE  
Digital level shifter with reduced power dissipation and false transmission blocking

PTO-2040  
12/89

<b>ISSUING CLASSIFICATION</b>									
<b>ORIGINAL</b>			<b>CROSS REFERENCE(S)</b>						
<b>CLASS</b>	<b>SUBCLASS</b>		<b>CLASS</b>	<b>SUBCLASS (ONE SUBCLASS PER BLOCK)</b>					
<b>INTERNATIONAL CLASSIFICATION</b>									

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>			<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)			<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____  _____  _____	_____ (Primary Examiner) (Date)			<b>ISSUE FEE</b>	
				Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)			<b>ISSUE BATCH NUMBER</b>	
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